

Docket No.: GR 97 P 1593 P

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Andreas Lenniger et al.

Filed : Concurrently herewith

Title : Power Semiconductor Module With Ceramic Substrate

82/5-23-00
jc568 U.S. PTO
09/436598
11/09/99

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U.S. Patent No. 5,408,128 (Furnival), dated April 18, 1995;

German Patent No. 43 38 107 C1 (Köhler et al.), dated March 9, 1995, pertains to a semiconductor module;

German Published, Non-Prosecuted Patent Application No. 36 04 313 A1 (Bayerer), dated August 13, 1987, pertains to a power semiconductor module;

European Patent Application No. 0 513 410 A1 (Neidig et al.), dated November 19, 1992, pertains to a power semiconductor module and method of producing such a module;

European Patent Application No. 0 438 165 A2 (Deie), dated July 24, 1991;

French Patent Application No. 2 660 826 (Dupont et al.), dated October 11, 1991, pertains to an economical box for electronic power components to be fixed onto a thermic dissipater and method for producing it;

French Utility Model No. 83 17799 (Leukel et al.), dated May 11, 1984, pertains to a power transistor module;

Japanese Patent Abstract No. 07153906 A (Nakamura et al.), dated June 16, 1995;


Japanese Patent Abstract No. 04326753 (Imanaka), dated November 16, 1992;

Japanese Patent Abstract No. 04111346 (Yoshimatsu), dated April 13, 1992;

Japanese Patent Abstract No. 60157243 (Takagi), dated August 17, 1985

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,



For Applicants

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Date: November 9, 1999

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